800.0039



PATENT #13

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Pechanek et al.

Serial No.:

09/422,015

Filed:

October 21, 1999

For:

METHODS AND APPARATUS FOR

ABBREVIATED INSTRUCTION SETS ADAPTABLE TO CONFIGURABLE

PROCESSOR ARCHITECTURES

(as amended)

Group:

2183

Examiner:

K. Kim

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on the date set forth below:

Signed: Maraya

Name: Marianna Tortorelli

Date: <u>(1911) 25, 2002</u>

Chapel Hill, North Carolina April 25, 2002

Commissioner for Patents BOX ISSUE FEE Washington, D.C. 20231

Transmittal of Twenty Sheets of Formal Drawings

Sir:

Enclosed are twenty sheets of formal drawings (Figs. 1A-10) for the above identified case.

Respectfully submitted,

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1/02